1. Prevent gate inhibit at reduced board supply voltage.
2. Reduce soft-start enable delay time to Rev. K enable delay time.
3. Ensure phase loss inhibit when SCR circuit is disconnected from 3-phase supply.
4. Limit current through D11 and allow lowpass filter with C26.

CHANGE RECORD

Date: 03-05-08
RCB Rev.: R
by: srs

1. Change PL detection circuit: A’/B’ PL circuit:
   - Re-introduce window comparator: R17, R18 & R404.
   - Add R26 & C16
   - Add R16 & C15
   - Add Q11, Q10 & R25
   - Remove R33 on Sig. Hi input
   - Change R10 & R11.
   - Change R13 & C7.
   - Rename components.

Date: 01-05-07
RCB Rev.: P
by: gb

1. Replace D6 with 0 Wm R600 resistor.
2. Replace D5 with a 9.1 V zener.
3. Change C10 from 10 to 0.022 uF.
4. Added R34 on Sig. Hi input

Date: 12-19-06
RCB Rev.: 0
by: srs

1. Add power-on inhibit clamp circuit: RNB. Q8, Q10, C28 and D6.
2. Move C13 from U4 to U4S.

Date: 09-19-06
RCB Rev.: N
by: drs

Removed C28 and the larger of the two transformer footprints. Repositioned several parts.

REV01 (see table)

RN60 RESISTORS (KΩ)

<table>
<thead>
<tr>
<th>RN60</th>
<th>Value (Ω)</th>
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<tbody>
<tr>
<td>R10</td>
<td>2.22</td>
</tr>
<tr>
<td>R11</td>
<td>2.10</td>
</tr>
<tr>
<td>R12</td>
<td>3.32</td>
</tr>
<tr>
<td>R13</td>
<td>12.4</td>
</tr>
<tr>
<td>R14</td>
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<td>32.4</td>
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<td>R24</td>
<td>100.0</td>
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</tbody>
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SCHEMATIC DIAGRAM: GENERAL PURPOSE 3-PHASE FIRING CIRCUIT

P.N. FC006100
Rev. R
E0128R

Sheet 2 of 2